

**Complete if Known**

(Use as many sheets as necessary)

Sheet	1	of	1
-------	---	----	---

Application Number	10/816,179
Filing Date	March 31, 2004
First Named Inventor	Chung J. Lee
Art Unit	1763
Examiner Name	Rudy Zervigon
Attorney Docket Number	DSI302

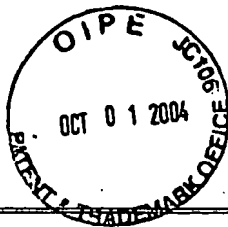
[illegible][illegible]

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

SHEET 1 OF 1

FORM PTO-1449		DOCKET NUMBER DSI 302		APPLICATION NUMBER 10/816,179		
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		APPLICANTS Chung J. Lee et al.				
		FILING DATE March 31, 2004		GROUP ART UNIT 1763		
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FIL. DATE IF APPROP.
12 ↓	3,503,903	3/31/70	Shaw et al.			
	6,086,952	7/11/00	Lang et al.			
	6,130,171	10/10/00	Gomi			
	6,265,320	7/24/01	Shi et al.			
	6,455,443	9/24/02	Eckert et al.			
	6,495,208	12/17/02	Desu et al.			
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
OTHER DOCUMENTS						
	Parylene Copolymers, Taylor et al., <u>Low Dielectric Constant Materials III</u> , pp. 197-205, 1997.					
12 ↓	Finer Copper Wires Make for Faster Integrated Circuits, Preuss, <u>Research News</u> , pp. 1-3, April 5, 1999.					
	Study of Hydrogen Annealing of Ultrahigh Molecular Weight Polyethylene Irradiated with High-Energy Protons, Wilson et al., <u>Journal of Materials Research</u> , Vol. 14, No. 11, November 1999.					
	A Novel Oxazole Based Low k Dielectric Addresses Copper Damascene Needs, Schmid et al., <u>Semiconductor Fabtech</u> , 12 th Edition, pp. 231-235, July 2000.					
	The Effect of Water Desorption and Organosilane Coupling Agents on the Adhesion of Poly(p-xylylene) Films to a Silicon Wafer Surface, Lightfoot et al., <u>Journal of Materials Science: Materials in Electronics</u> , Vol. 12, pp. 581-586, 2001.					
↓	Current Technical Trends: Dual Damascene & Low-k Dielectrics, Healey on behalf of Threshold Systems, pp. 1-6, © 2002.					
EXAMINER		DATE CONSIDERED				